



Certificate / Certificat Zertifikat / 合格証

WES 1601024 C001

exida hereby confirms that the:

Digital EPIC2 Position Transmitter

**Westlock Controls
Saddle Brook, NJ - USA**

Has been assessed per the relevant requirements of:

IEC 61508 : 2010 Parts 1-7

and meets requirements providing a level of integrity to:

Systematic Integrity: SC3 (SIL 3 Capable)

Random Integrity: Type A Non-interfering

**PFD_{AVG} and Architecture Constraints
must be verified for each application**

Safety Function:

When used in series with a logic solver output the device can perform partial stroke proof testing and will not interfere with the associated valve moving to a safe state nor with the logic solver moving the valve to a safe (de-energized) state.

Application Restrictions:

The unit must be properly designed into a Safety Instrumented Function per the Safety Manual requirements.



Evaluating Assessor

Certifying Assessor

The manufacturer may use the mark:



Revision 2.1 August 29, 2022
Surveillance Audit Due
March 1, 2025



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Systematic Capability: SC 3 (SIL 3 Capable)

Random Capability: Type A Non-Interfering

**PFD_{AVG} and Architecture Constraints
must be verified for each application**

Digital EPIC2 Position
Transmitter

Systematic Capability :

The product has met manufacturer design process requirements of Safety Integrity Level (SIL) 3. These are intended to achieve sufficient integrity against systematic errors of design by the manufacturer.

A Safety Instrumented Function (SIF) designed with this product must not be used at a SIL level higher than stated.

Random Capability:

The SIL limit imposed by the Architectural Constraints must be met for each element. This device meets *exida* criteria for Route 2_H.

IEC 61508 Failure Rates in FIT*

Device	λ_{SD}	λ_{SU}	λ_{DD}	λ_{DU}
D-EPIC2 HART	1	3390	0	4

* FIT = 1 failure / 10⁹ hours

SIL Verification:

The Safety Integrity Level (SIL) of an entire Safety Instrumented Function (SIF) must be verified via a calculation of PFD_{avg} considering redundant architectures, proof test interval, proof test effectiveness, any automatic diagnostics, average repair time and the specific failure rates of all products included in the SIF. Each element must be checked to assure compliance with minimum hardware fault tolerance (HFT) requirements.

The following documents are a mandatory part of certification:

Assessment Report: WES 16/01-024 R001 V2 R1

Safety Manual: VCOSI-04978-EN HR; Mar 24,2017 or later



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